

**REMARKS**

Applicant thanks the Examiner for his courtesy and assistance in the interview which was held on 10 August 2005. In view of the discussion held with the Examiner, Applicant submits herewith a replacement set of claims in which independent Claims 1, 2, 13, and 14 are cancelled and replaced, respectively, with new claims 27-30. The dependency of the claims depending from Claims 2 and 14 has been corrected.

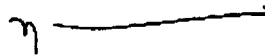
Applicant's replacement claims are deemed novel and unobvious in view of the art of record and are presented as less verbose forms of allowed Claims 7 and 20.

During the Interview, the Examiner stressed that applicant should provide an indication of support in the specification for the claim amendments. In this regard, the Examiner is directed to Figure 2, and page 20, line 16 to page 22, line 7 (debugging analog and mixed signal behavioral models of circuit designs, and transient analysis time advancement algorithms); page 25, line 22 (non-ordinary differential equation editor solver); page 22, line 16 + (interactive model behavior verification); page 23, line 15 (acceptable timepoints); and page 24, lines 4-10 (debugging breakpoints).

Applicant also notes that a reading of the Specification fully supports the newly added claims, and the references to the specification are merely illustrative of the support found in the Specification. The Examiner is also directed to Applicant's discussion of the prior art in which various known techniques which are incorporated into the claims are described.

Should the Examiner deem it helpful, he is encouraged to contact Applicant's attorney, Michael A. Glenn at 650-474-8400.

Respectfully submitted,



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